

WEST Search History

DATE: Thursday, January 13, 2005

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		<i>DB=USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L9	L8 same control\$4	14
<input type="checkbox"/>	L8	L7 same model\$4	54
<input type="checkbox"/>	L7	l4 same L5	2020
<input type="checkbox"/>	L6	l4 and L5	9337
<input type="checkbox"/>	L5	((energy or power) near3 (consum\$9 or use or usage or utiliz\$9))	426488
<input type="checkbox"/>	L4	(verif\$9 or check\$4 or calculat\$4 or determin\$4 or deriv\$4) near3 (use or usage or utiliz\$9)	89343
<input type="checkbox"/>	L3	l1 same (energy or power\$4)	5
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L3: Entry 2 of 5

File: USPT

Jul 22, 2003

DOCUMENT-IDENTIFIER: US 6598029 B1

**** See image for Certificate of Correction ****

TITLE: Bidding for energy supply with request for service

Detailed Description Text (30):

Depending on the type of end user or reseller and the needs of the Provider (and, perhaps, the end user's DISCO), the frequency at which actual usage reports should be fed back to the selected Provider or DISCO will vary. For example, very large users of electric power can create temporary imbalances in the local power grid and contribute to meaningful fluctuations in the aggregate amount of power required to be supplied by a selected Provider to meet the needs of all of its customers in a particular service area. The DISCO for that end user will also want to obtain timely usage information in order to manage such imbalances on its local grid effectively. Frequent meter readings would be desirable for this type of customer. On the other hand, residential customers as a group have fairly predictable usage profile patterns and would require much less frequent monitoring. The Moderator will process and transmit such actual usage reports at such frequencies as are specified in the auction rules, with reasonable exceptions accommodated at the request of the selected Provider or DISCO. In addition, to facilitate such end user's or reseller's energy management efforts, the Moderator (or applicable control computer) can also transmit actual energy usage data (with or without current information on bid prices) on a periodic basis back to the end user or reseller (to be received by the end user's meter or such other terminal equipment as the end user or reseller may designate) or, in the alternative, the Moderator (or applicable control computer) can transmit such data to an electronic mail address or Internet website designated by the end user or reseller.

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L9: Entry 3 of 14

File: USPT

Sep 17, 2002

DOCUMENT-IDENTIFIER: US 6453443 B1

TITLE: Method for cell modeling and timing verification of chip designs with voltage drop

Brief Summary Text (6):

In U.S. Pat. No. 6,088,523 (Nabors et al.) a method and apparatus is directed for simulating an electrical circuit design using approximate circuit element tapers. In U.S. Pat. No. 5,949,689 (Olson et al.) is directed to a method and system for modeling power consumed by a logic cell in a computer controlled power estimation process. In U.S. Pat. No. 5,930,148 (Bjorksten et al.) a method and system for verifying is directed a digital circuit design including dynamic circuit cells that utilize diverse circuit techniques. In U.S. Pat. No. 5,878,053 (Koh et al.) hierarchical power network simulation and analysis tool is directed toward reliability testing of deep sub micron IC designs. However, the simulation and analysis does not include timing delay data. In U.S. Pat. No. 5,872,952 (Taun et al.) a method is directed toward power net analysis of integrated circuits. A power net simulator uses current values to calculate characteristics of the power network. In U.S. Pat. No. 5,598,348 a method and apparatus is directed to analyzing the power network of a VLSI circuit, but does not calculate cell timing delay data. In U.S. Pat. No. 5,471,409 (Tani) a logic and circuit simulation apparatus is directed to simulation based on signal propagation delay time and high reliability. However, the simulation does not use a voltage drop cell timing model. In U.S. Pat. No. 5,446,676 (Huang et al.) a method is directed to transistor level timing, power simulator and power analyzer.

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